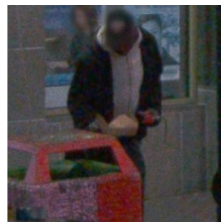
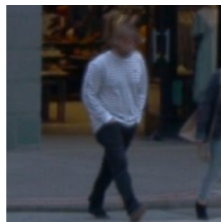
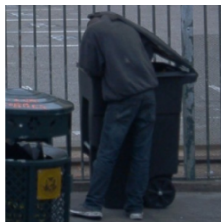
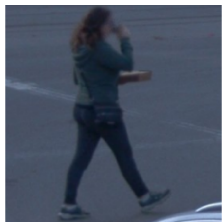
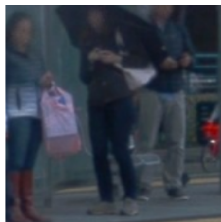
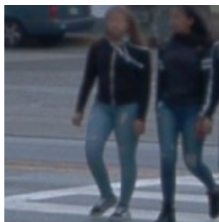
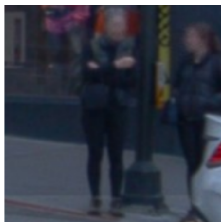


Inputs

RGB

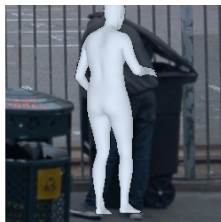
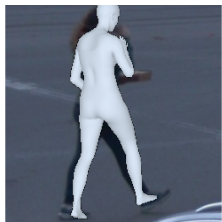
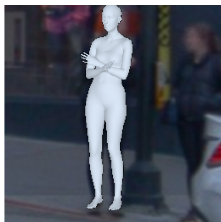


PC

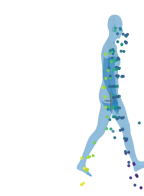
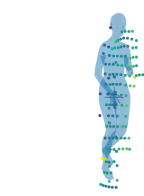
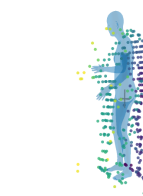
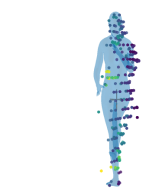
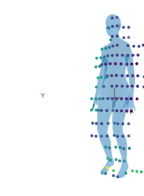
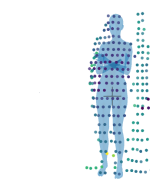


Outputs

SMPL + RGB



SMPL + PC



a)

b)

c)

d)

e)

f)

g)

h)